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PTO/SB/123 (01-06)

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Patent Number	6,891,774
Issue Date	5/10/2005
Application Number	10/654,561
Filing Date	9/9/2003
First Named Inventor	Shahram Abdollahi-Alibeik
Attorney Docket Number	1002-1

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☐ Attorney or agent of record. Registration Number _____

Signature

Typed or
Printed Name Scott Robins

Date

24 JAN 2008

Telephone (408) 597-3660

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.

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PTO/SB/56 (01-08)

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STATEMENT UNDER 37 CFR 3.73(b)Applicant/Patent Owner: Shahram Abdollahi-Alibeik, Chaofeng HuangApplication No./Patent No.: 6,891,774 Filed/Issue Date: 5/10/2005Entitled: Delay Line and Output Clock Generator Using sameT-RAM, Inc. _____ a _____ corporation
(Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of less than the entire right, title and interest
(The extent (by percentage) of its ownership interest is _____ %)

in the patent application/patent identified above by virtue of either:

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☐ Additional documents in the chain of title are listed on a supplemental sheet.☐ As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.

(NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08)

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

Signature

Scott Robins

Printed or Typed Name

Vice President, T-RAM Semiconductor, Inc. (formerly T-RAM, Inc.)

Title

24 JAN 2008

Date

(408) 597-3660

Telephone Number

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